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- L1: (2) (("5513385") or ("5475863")).PN.
- L2: (1) 1 and (buffer\$3 or memory)
- L3: (0) 2 and "matched condition signal"
- L4: (14641) 455/184.1 455/183.2 455/73 455/95 455/185.1 455/18
- L5: (4451) 340/825 340/3.3 340/3.32 340/7.32 340/7.52 340/825.2
- L7: (18738) 4 or 5
- L8: (107) 7 and (supplementary near2 data)
- L9: (12) 8 and decoder
- L10: (4) 9 and word
- L11: (1) 10 and (match\$3 near8 "condition signal")
- L12: (3) 10 and bit
- L13: (1) 12 and (detect\$3 near5 circuitry)
- L14: (36) 7 and (supplementary near2 data).clm.
- L15: (3) 8 and decoder.clm.
- L16: (1) 10 and (match\$3 near8 "condition signal").clm.

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- S1: (25380) (RF or "radio frequency") near3 receiver
- S2: (12204) S1 and (stream\$ or bit)
- S3: (507) S2 and "radio station"

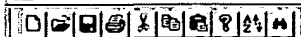
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1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050100116 A1	20050512	14	Reduced power consumption	375/316	455/184.1		Mason, Ken

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- S4: (5) S3 and (word near8 match\$3)
- S5: (457) S3 and detect\$3
- S6: (45) S3 and (detect\$3 near3 circuitry)

United States

Patent Application Publication (9) Pub. No.: US 2005/0100116 A1

Mason (2) Pub. Date: May 12, 2005

(4) REDUCED POWER CONSUMPTION

(7) Inventor: Ken Mason, Thompsonville, CT

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(7) Assignee: Nalco Corporation

(2) Appl. No.: 09/611,411

(2) Filed: Oct. 22, 2002

(3) Foreign Application Priority Data

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Publication Classification
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(52) U.S. Cl. 375/316, 455/184.1

(57) ABSTRACT

A radio frequency receiver for receiving supplementary data transmitted with a radio station signal, and supplementary

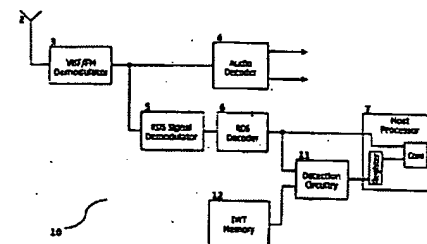
data comprising one or more information word fields comprising supplementary data transmitted with the radio station signal, and various embodiments.

A first processor for processing and supplementary data.

A supplementary data demodulator and decoder for demodulating and decoding a received radio signal or generate a stream of bits representing one or more information word fields.

A second processor for processing one or more information word fields.

A detection circuitry arranged to receive and compare the information word fields received by the supplementary data demodulator with one or more information word fields stored in the information word fields memory, and to generate a matched condition signal in response to a match between one or more detected information word fields and one or more of said complementary stored information word fields, and wherein the detection circuitry is arranged to, in response to the matched condition signal, cause the supplementary data to be processed by means of the first processor.



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1			US 20050100116 A1	20050512	14	Reduced power consumption	375/316	455/184.1		Mason, Ken

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— S5: (457) S3 and detect\$3

— S6: (45) S3 and (detect\$3 near3 circuitry)

— S7: (12) S6 and "data decoder"

- S8: (12) S7 and word

— S9: (2) S8 and (supplementary near2 data)

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455/127.1 455/412.1 455/418 455/458 455/514 455/522
455/550.1 455/556.2 455/574 455/130 455/334 455/343.1
455/343.2 455/343.5

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- L11: (11) 5 and (word near3 field)
- L12: (75850) "455"/\$.ccls.
- L13: (6270) 12 and ((RF or "radio frequency") near3 receiver)
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- L17: (16) 15 and word
- L18: (15) 17 and detect\$3
- L19: (4) 18 and (word near8 match\$3)
- L20: (2) 1 and "information word field"

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1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050100116 A1	20050512	14	Reduced power consumption	375/316	455/184.1		Mason, Ken
2	<input type="checkbox"/>	<input type="checkbox"/>	WO 2005039079 A1	20050428	33	REDUCED POWER CONSUMPTION				MASON, KEN

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- L11: (11) 5 and (word near3 field)
- L12: (75850) "455"/\$.ccls.
- L13: (6270) 12 and ((RF or "radio frequency") near3 receiver)
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- L17: (16) 15 and word
- L18: (15) 17 and detect\$3
- L19: (4) 18 and (word near8 match\$3)

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18 and (word near8 match\$3)

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1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050100116 A1	20050512	14	Reduced power consumption	375/316	455/184.1		Mason, Ken
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5475863 A	19951212	22	Method and apparatus for delivering messages to portabl	455/458	340/7.22; 340/7.26;		Simpson; Floyd et al.
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5428388 A	19950627	25	Video doorbell system	348/155	348/156; 379/102.0		von Bauer, Richard P. et al.
4	<input type="checkbox"/>	<input type="checkbox"/>	US 4367557 A	19830104	27	Wired broadcasting systems	725/149	340/310.1		Stern; Joseph L. et al.

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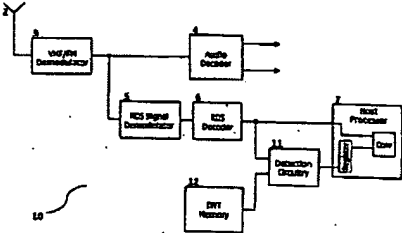
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13 230600N 1041	
(26) United States (25) Patent Application Publication (24) Name	(23) Pub. No.: US 2005/0108116 A1 (22) Pub. Date: May 12, 2005
(10) EXAMINED PAPER CONVENTION (11) Inventor: Ron Memon, Danvers, CO (12) Correspondence Address: HARRINGTON & SMITH, LLP 4 BANGS BLVD. BRISTOL, CT 06010-4221 (US) (27) Assignee: Pella Corporation (28) Appl. No.: 09/585,123 (29) Filed: Oct. 22, 2003 (30) Foreign Application Priority Date: Oct. 16, 2003 (US) 62/286,000	
Publication Classification (31) Int. Cl. ⁷ : H04B 1/10 (32) U.S. Cl.: 380/316, 432/161 (33) ARTHUR	
(34) A radio frequency receiver for receiving supplementary data transmitted with a radio voice signal, and supplementary	
<div style="display: flex; justify-content: space-between;"> <div style="width: 45%;"> <p>data comprising one or more information word fields carrying supplementary data transmitted with the radio-voice signal, and receiving comprising:</p> <ul style="list-style-type: none"> a first processor for processing said supplementary data; a supplementary data demodulator and decoder for demodulating and decoding a modulated radio signal to generate a stream of bits representing one or more information word fields; a intermediate word type processor for storing one or more information word type fields; broken stream attempt to receive and process the information word type field demodulated by the supplementary data demodulator with one or more information word type fields stored in the information word type processor; and at least a matched multiplier signal in response to a search to select one or more demodulated information word type fields and one or more of said corresponding stored information word type fields, and wherein one outcome is accepted as, in response to the matched multiplier signal, means for demodulating supplementary data to be processed by means of the first processor. </div> <div style="width: 50%;">  </div> </div>	

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